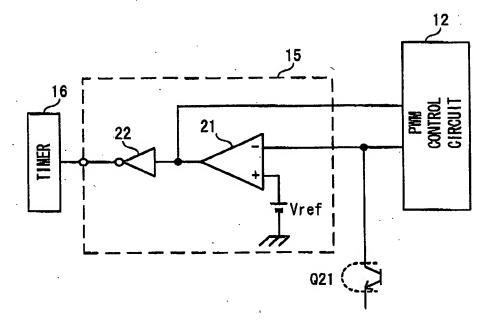


2/9 FIG. 2



3/9 FIG. 3

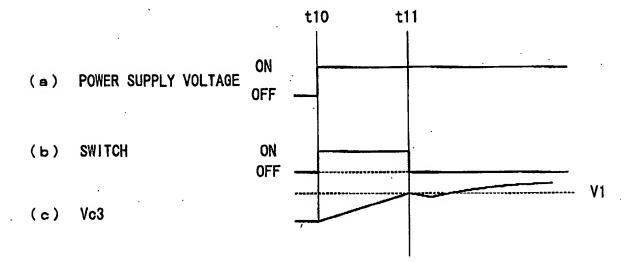
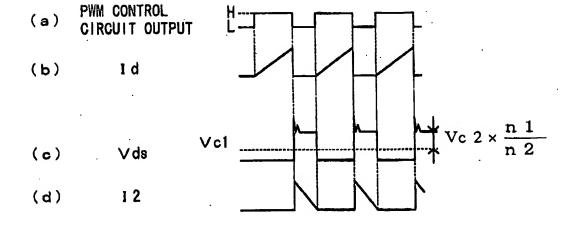
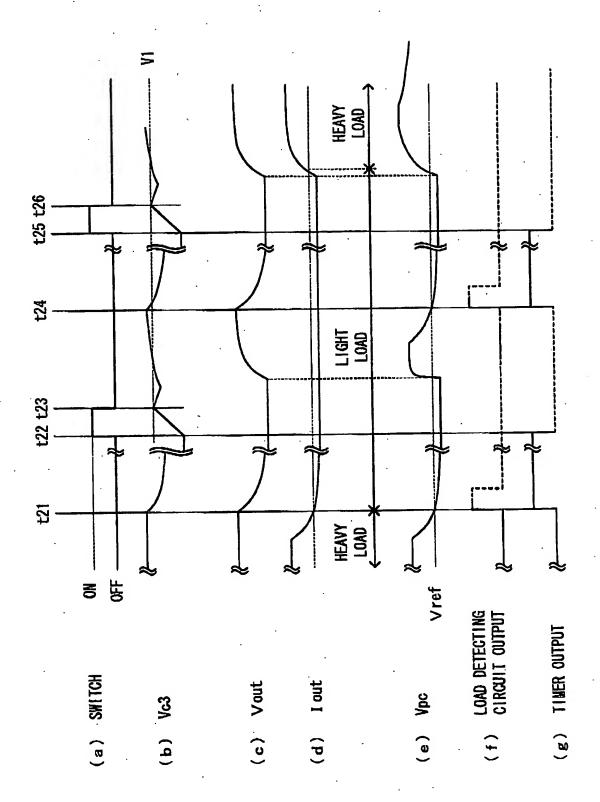
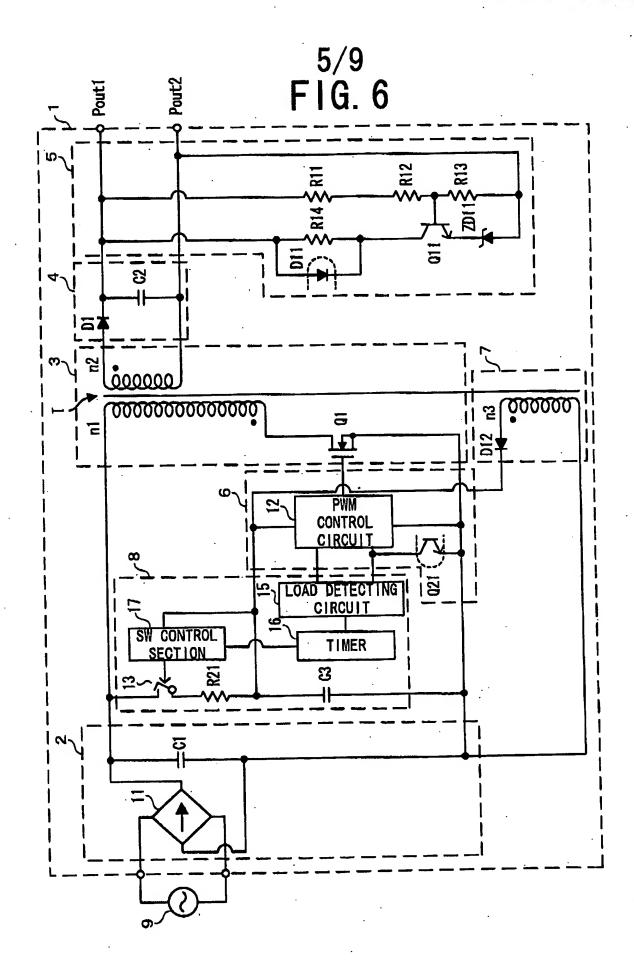


FIG. 4

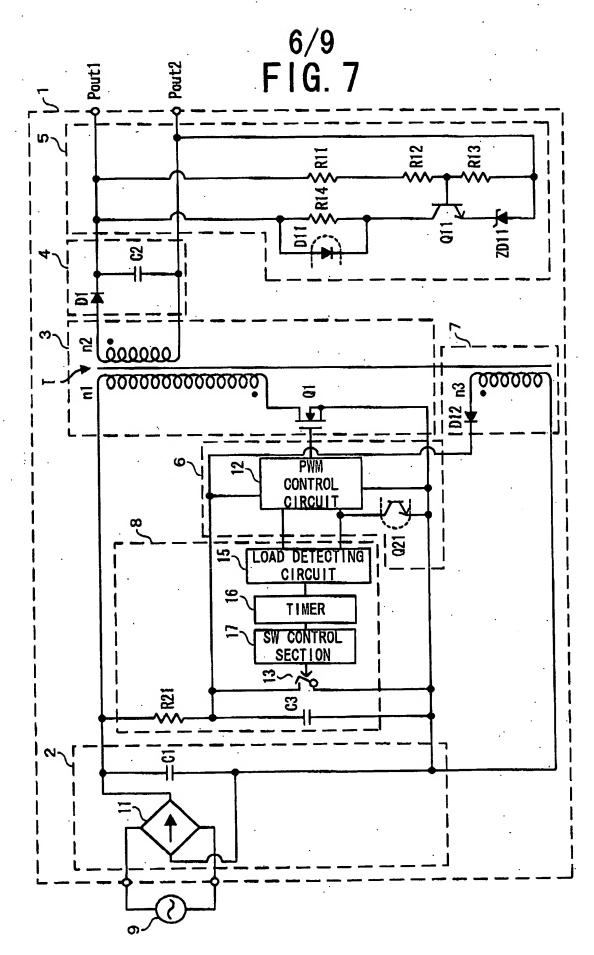


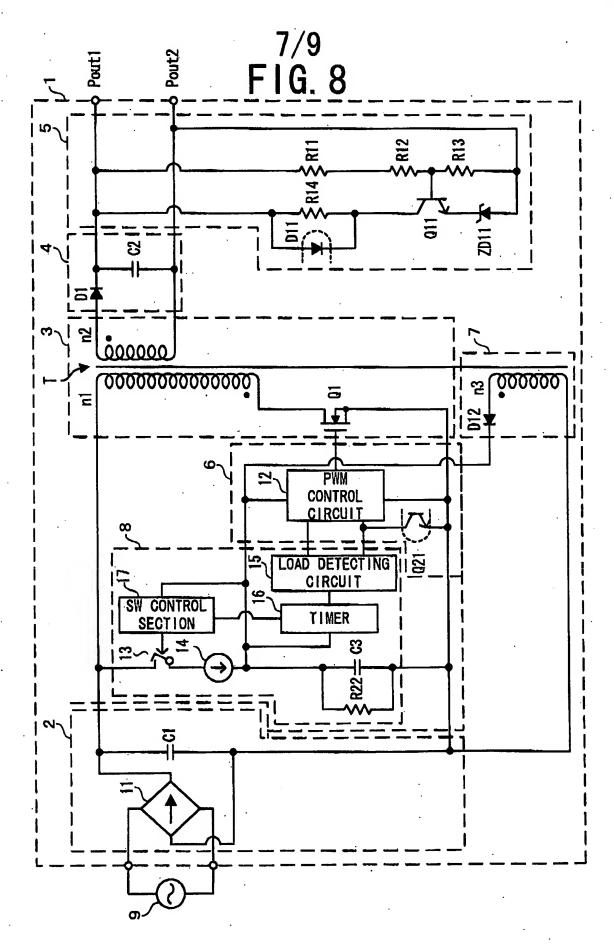
4/9 FIG. 5



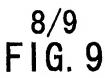


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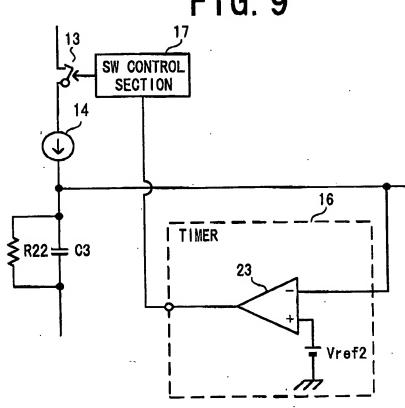
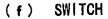
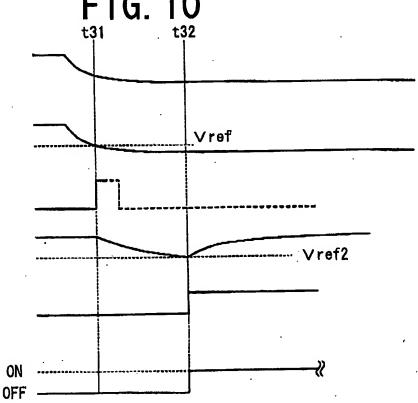


FIG. 10



- (b) Vpc
- LOAD DETECTING CIRCUIT OUTPUT (c)
- (d) Vc3
- (e) TIMER OUTPUT





9/9 FIG. 11

